

## **REMARKS/ARGUMENTS**

Applicants have received the Office action dated September 28, 2005, in which the Examiner: 1) objected to the drawings under 37 C.F.R § 183(a) as not showing every feature of the invention specified in the claims; and 2) rejected claims 22-31 under 35 U.S.C. § 103(a) as being unpatentable over Lee (U.S. Pat. No. 6,405,320) in view of Reneris (U.S. Pat. No. 6,209,088). With this Response, Applicants modify the drawings, the written description, and claims 22-24, 26-27, and 30-31 to address the drawing objections, and traverse the obviousness rejections.

### **I. THE DRAWING OBJECTIONS**

In response to the Examiner's objection to the drawings as not showing every feature of the invention specified in the claims, Applicants have: 1) modified claims 22 and 26 to no longer refer to "pointing the host processor to an instruction"; 2) modified Figure 7 to reflect writing both a recognized and unrecognized sleep type as detailed in the written description and recited in claims 22 and 26 as modified; 3) modified claims 23 and 27 to recite "changing the power state of the microcontroller to a low power device state"; 4) modified claim 24 to recite "changing the power state of the system comprises shutting down the microcontroller"; 5) modified the written description to clarify how changing the power of the microcontroller to a low power device state and powering down the microcontroller, both actions already included and detailed in the written description, are actions that are encompassed by step 722 of Figure 7; and 6) modified claims 30 and 31 to recite "transitioning out of the low power state and writing the first sleep type to the second register" as detailed in the written description. None of these modifications add any new matter. Applicants respectfully submit that as a result of these modifications the drawings show every detail of the invention specified in the claims as modified, and thus the drawings are allowable.

### **II. THE OBVIOUSNESS REJECTIONS**

The present application claims priority to parent application Serial No. 09/539,061, filed on March 30, 2000, and now issued as U.S. Patent.

No. 6,732,280. The '280 patent in turn claimed priority to Singapore Application No. 9903600-6, filed July 26, 1999. Thus, the present application is entitled to priority of at least as early as March 30, 2000, if not July 26, 1999. The Examiner primarily relied on Lee in the obviousness rejections. Lee was filed in 1998 and issued in 2002. The filing and issue dates of Lee are such that Lee qualifies as prior art, if at all, under 35 U.S.C. § 102(e). At the time the present invention was made, the present invention and Lee were commonly owned or subject to an obligation of assignment to the same person (Compaq Computer Corp.). Thus, per 35 U.S.C. § 103(c)(1), Lee cannot be used as prior art against the present claims in an obviousness analysis and Reneris does not satisfy the void left by Lee. Further, the Examiner evidently concluded that Lee does not anticipate the claims. Thus, for at least the reason that Lee is unusable as prior art, all claims are allowable.

### **III. CORRECTION OF A TYPOGRAPHICAL ERROR**

Applicants further have modified the specification to correct an unintentional typographical error regarding a reference to the D4 system state. The written description should have instead referred to the S4 system state, as there is no D4 system state. This correction does not add any new matter to the disclosure.

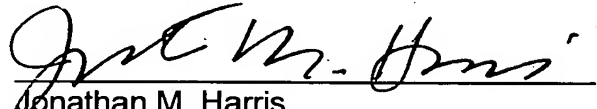
### **IV. CONCLUSION**

Applicants respectfully request reconsideration and that a timely Notice of Allowance be issued in this case. It is believed that no extensions of time or fees are required, beyond those that may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary to allow consideration of this paper, such extensions are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required (including

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fees for net addition of claims) are hereby authorized to be charged to Hewlett-Packard Development Company's Deposit Account No. 08-2025:

Respectfully submitted,



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**Amendments to the Drawings:**

The attached sheet of drawings includes changes to Fig. 7. In Figure 7, the descriptions in blocks 702 and 724 have been corrected.

Attachment: Replacement Sheet  
Annotated Sheet Showing Changes

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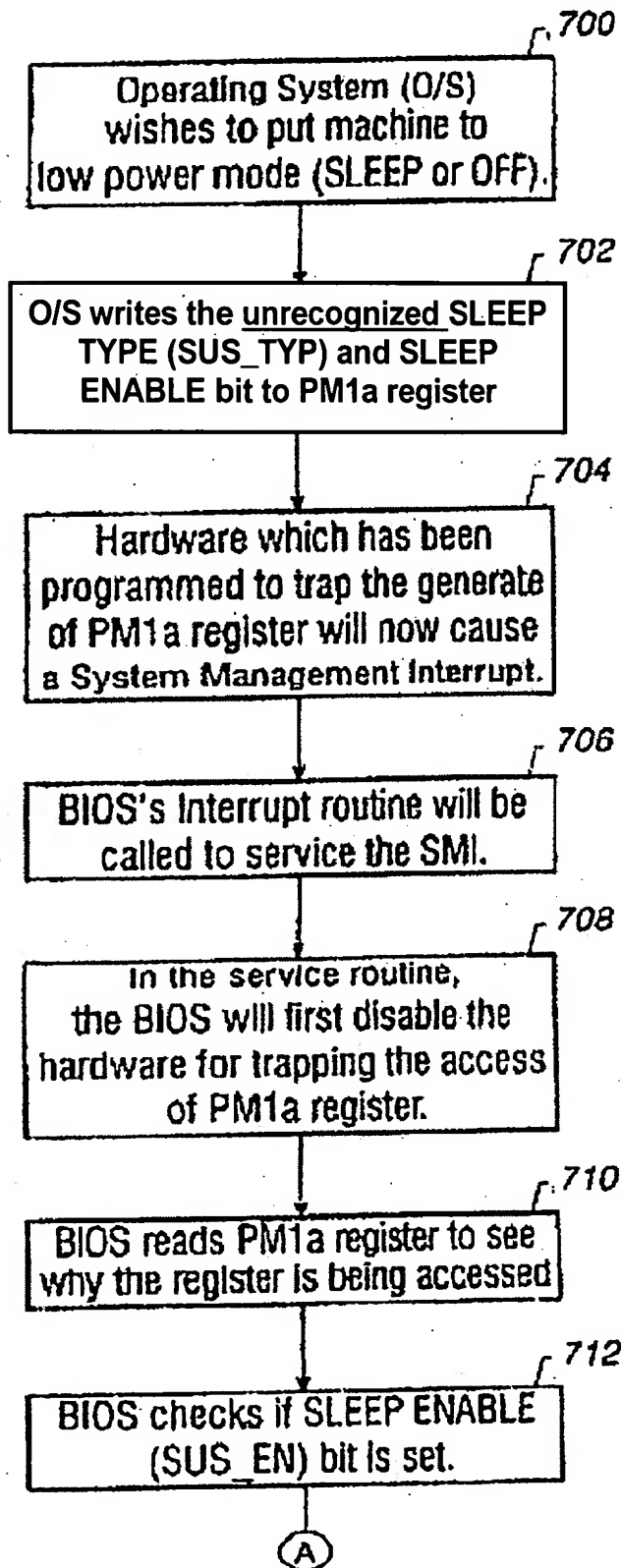


FIG. 7

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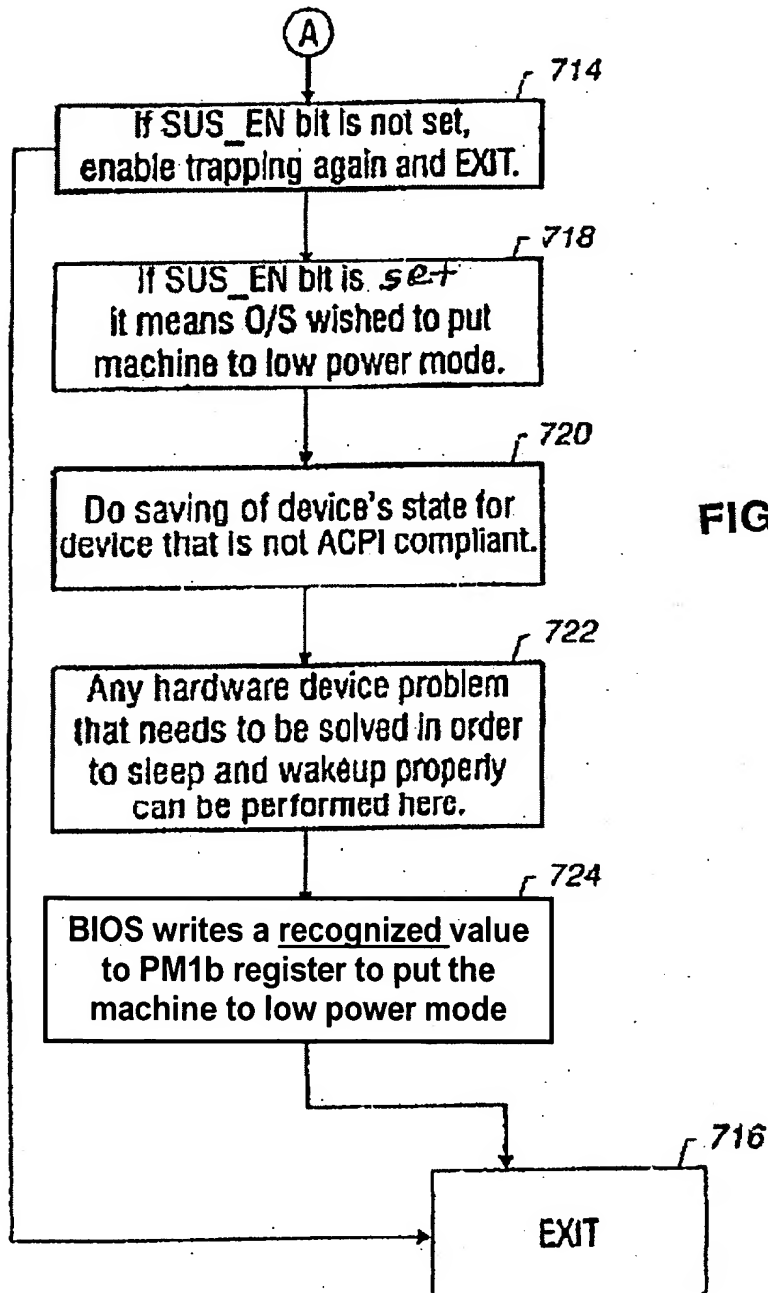


FIG. 7 (continued)